Software Versions

Release 3.1 (January 2014)
- Added support for NTP Stratum 2
- Fixed LCD update rate when not locked to any valid source

Release 3.0 (November 2013)
- Fixed PTP offset issue introduced in version 2.6

Release 2.9 (October 2013)
- Added support for PPS over serial line

Release 2.8 (October 2013)
- Added support for NTP hardware timestamping

Release 2.7 (September 2013)
- Added multiple VLAN support

Release 2.6 (August 2013)
- Updated PTP settings

Release 2.5 (April 2013)
- Updated GUI to fix some problems in the E1 panel

Release 2.4 (March 2013)
- Added support for NMEA GPS radio

Release 2.3 (February 2013)
- Resync of the PPS when disciplining in frequency only when necessary

Release 2.2 (January 2013)
- Software libraries updated

Release 2.1 (January 2013)
- Added support for PTP over Layer 2 and VLAN (802.1Q)

Release 2.0 (January 2013)
- Added PTP custom parameters
- Added disciplining parameters in the PTP Announce Message
- Added support for PTPv2 unicast through signalling messages

Release 1.9 (November 2012)
- Added timing information over E1

Release 1.8 (October 2012)
- Added handling of the ESMC packets for the SyncE

Release 1.7 (October 2012)
- Added clock generation support

Release 1.6 (September 2012)
- SyncE management simplified

Release 1.5 (August 2012)
- NTP server even without GPS
- Auto-recognition of the IRIG-B input
- IRIG-B robustness increased

Release 1.4 (June 2012)
- Enhanced recognition of the external frequency

- Added support for hardware upgrade

Release 1.3 (April 2012)
- Enhanced disciplining of the NTP server

Release 1.2 (March 2012)
- Added support for Synchronous Ethernet

Release 1.1 (February 2012)
- Added support for IRIG-B

Release 1.0 (December 2011)
- First release

Hardware Versions

Release 23 (November 2013)
- Improved data bus integrity

Release 22 (November 2013)
- Compiled with new development tools

Release 21 (September 2013)
- Cleaned pulse generation code
- GPS fake through external input

Release 20 (June 2013)
- Fix for PPS muting

Release 19 (May 2013)
- Improved timing stability

Release 18 (May 2013)
- Internal fix for E1 component

Release 17 (April 2013)
- Fixed boot isssues

Release 16 (March 2013)
- Improved locking stability between 10 MHz and 2.048 MHz

Release 15 (February 2013)
- IRIG-B muted until stable

Release 14 (February 2013)
- Added fast lock over SyncE

Release 13 (November 2012)
- Added timing information over E1 line

Release 12b (October 2012)
- Improved stability of processor bus

Release 12 (October 2012)
- Improved support for SyncE holdover

Release 11b (October 2012)
- Improved pps resync when doing frequency locking

Release 11 (October 2012)
- Added support for custom clock generation

Release 10 (September 2012)
- Added support for SyncE holdover

Release 9 (June 2012)
- Added support for updating hardware release (not backward compatible)

Release 8 (May 2012)
- Improved recognition of external frequency

Release 7 (April 2012)
- Two programmable pulses are supported
- Added mux for PPS and IRIG-B

Release 6 (April 2012)
- Added programmable pulses

Release 5 (March 2012)
- Added Synchronous Ethernet

Release 4c (March 2012)
- Improved reset handling

Release 4b (February 2012)
- Fixed resync
- IRIG-B AM phase fixed

Release 4 (February 2012)
- First version